

Overview

The UP2000 scales the UP1000's architecture to one or two Alpha 21264 processors sharing a wide, ECC-protected memory bus — built for clustered and high-availability Linux/Alpha deployments that outgrow a single-processor board.

Processor & Cache

- One or two Alpha 21264 processors, Slot B modules, 667 or 750MHz
- 2, 4, or 8MB L2 cache per processor; 3.2GB/s sustained L2 cache bandwidth
- Integrated I²C bus for system management; on-module thermal sensor per processor

Chipset & Memory

- 256-bit wide memory bus, up to 2.65GB/s memory bandwidth
- Up to 8 PC100 168-pin registered/buffered SDRAM DIMMs with ECC, 256MB–2GB
- Dual independent peer PCI buses, 400MB/s total I/O bandwidth

Expansion & I/O

- Six PCI slots (two 64-bit, four 32-bit), one shared ISA slot
- Floppy controller, two serial ports (modem control), parallel port, dual USB
- 2MB flash ROM

Chassis & Form Factor

Extended-ATX, 13"x12", rack-mountable with the API PowerRAC Chassis 320 — up to 28 UP2000 systems per rack.

Safety & Handling

Disconnect power before installing or removing any component. Processor modules and DIMMs are static-sensitive — use an anti-static wrist strap and handle boards by their edges.

Warranty & Support

Standard warranty terms are available from your reseller or systems vendor. For technical support, call (978) 318-1181 or see the Support section of this site.